

UNITED STATES DEPARTMENT OF COMMERCE Patent and Trademark Office

: COMMISSIONER OF PATENTS AND TRADEMARKS

Washington, D.C. 202	231	RG.
		117.01

APPLICATION NO.	FILING DATE		FIRST NAMED INVENTOR		ATTORNEY DOCKET NO.	
09/475,7	13 12/30	/99	ZUNIGA		M	09464-01000
				\neg	EXAMINER	
			MMC2/09:	11		
FISH & R	CHARDSON I	PC			RTI FY S	
2200 SANI	HILL ROAL	D			ART UNIT	PAPER NUMBER
	SUITE 100 MENLO PARK CA 94025			2838	7	
					DATE MAILED:	09/11/00

Please find below and/or attached an Office communication concerning this application or proceeding.

Commissioner of Patents and Trademarks

	Application No.	Applicant(s)	
	09/475,713		
Office Action Summary	Examiner	Art Unit	
	Shawn Riley	2836	
The MAILING DATE of this communication appe Period for Reply	ars on the cover sheet wi	th the correspondence add	ress
A SHORTENED STATUTORY PERIOD FOR REPLY THE MAILING DATE OF THIS COMMUNICATION.	IS SET TO EXPIRE 3 N	IONTH(S) FROM	
 Extensions of time may be available under the provisions of 37 after SIX (6) MONTHS from the mailing date of this communities in the period for reply specified above is less than thirty (30) day be considered timely. If NO period for reply is specified above, the maximum statutory communication. Failure to reply within the set or extended period for reply will, b Status 	cation. s, a reply within the statutory m r period will apply and will expir	unamium of thany (30) days will a SIX (6) MONTHS from the mail	ling date of this
1) Responsive to communication(s) filed on	<u> </u>		
2a) This action is FINAL . 2b) ⊠ Thi	is action is non-final.		
3) Since this application is in condition for allowa closed in accordance with the practice under the condition of the condit			e merits is
Disposition of Claims			
4) Claim(s) 1-20 is/are pending in the application			
4a) Of the above claim(s) is/are withdra	wn from consideration.	,	
5) Claim(s) is/are allowed.			
6)⊠ Claim(s) <u>1-10 and 15-18</u> is/are rejected.			
7)⊠ Claim(s) <u>19 and 20</u> is/are objected to.			
8)⊠ Claims <u>11-14</u> are subject to restriction and/or	election requirement.		
Application Papers			
9) The specification is objected to by the Examine	er.		
10) The drawing(s) filed on is/are objected to	o by the Examiner.		
11) The proposed drawing correction filed on	_ is: a) ☐ approved b) [] disapproved.	
12) The oath or declaration is objected to by the Ex	kaminer.	·	
Priority under 35 U.S.C. § 119			
13) Acknowledgment is made of a claim for foreign	nriority under 35 U.S.C.	s 119(a)-(d)	
a) ☐ All b) ☐ Some * c) ☐ None of the CERTIF			
a) ☐ All b) ☐ Some c) ☐ None of the CERTIF	IED copies of the priority	documents have been.	
2. received in Application No. (Series Code	e / Serial Number)	.•	
3. received in this National Stage application	on from the International I	3ureau (PCT Rule 17.2(a))).
* See the attached detailed Office action for a list	of the certified copies not	received.	
14) Acknowledgement is made of a claim for dome	estic priority under 35 U.S	.C. & 119(e).	
Attachment(s)			
 15) Notice of References Cited (PTO-892) 16) Notice of Draftsperson's Patent Drawing Review (PTO-948) 17) Information Disclosure Statement(s) (PTO-1449) Paper No(s) 	19) Notice of	w Summary (PTO-413) Paper No of Informal Patent Application (PT	

Application/Control Number: 09/475,713 Page 2

Art Unit: 2838

DETAILED ACTION

1. Applicant's election with traverse of group I in Paper No. 6 is acknowledged. The traversal is on the ground(s) that the claims are not limited to a voltage regulator with only a single transistor and the inventions are not unrelated but are useable together as shown in figure 5. This is not found persuasive because figure 5 has nothing to do with the first and second groups being used as combination/subcombination. Figure 5 merely shows the fractional power loss as a function of device width of an NMOS operating at either 5 or 12 volts at the gate. Further, the previous office action made no reference to a combination/subcombination as rationale for the restriction. The restriction was based on unrelated invention, that is they are not disclosed as being used together. Applicants do not disclose a first regulator with a single transistor in series/parallel with a second regulator having two transistors (i.e., multiple regulators – not multiple transistors in a single regulator).

The requirement is still deemed proper and is therefore made FINAL.

Claim Rejections - 35 U.S.C. § 112

1. Claims 3, 4, 8 and 18 (and therefore claims 5-7 and 9-10) are rejected under 35 U.S.C. § 112, 2nd paragraph,

The specification shall conclude with one or more claims particularly pointing out and distinctly claiming the subject matter which the applicant regards as his invention.

In claims 3, 4, and 18, the recitation of the "first voltage" lacks antecedent basis.

In claim 3 and 18 the recitation of the "second voltage" lacks antecedent basis.

In claim 8 the "drive train" of the first and second transistors is not understood. Do applicants mean to refer to a driver circuit, i.e., a driver?

Application/Control Number: 09/475,713 Page 3

Art Unit: 2838

Claim Rejections - 35 U.S.C. § 102

2. The following is a quotation of the appropriate paragraphs of 35 U.S.C. § 102 that form

the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless --

(b) the invention was patented or described in a printed publication in this or a foreign country or in public use or

on sale in this country, more than one year prior to the date of application for patent in the United States.

3. Claims 1-2 and 15-17, and as far as understood claims 3-10 and 18 are rejected under 35

U.S.C. §102(b) as being fully anticipated by Plumton (U.S. Patent 5,554,561). Plumton shows,

(in, e.g., figure 3b and the corresponding disclosure) a first and second transistor, a controller and

a filter disposed between the input and the output wherein the first transistor is a PMOS and the

second transistor is an NMOS compatible with a logic voltage wherein the first transistor

includes a first gate oxide layer and the second transistor includes a second gate oxide layer, the

first gate oxide layer is thicker than the second gate oxide layer.

Allowable Subject Matter

4. Claims 19 and 20 are objected to as being dependent upon a rejected base claim, but

would be allowable if rewritten in independent form including all of the limitations of the base

claim and any intervening claims.

5. As allowable subject matter has been indicated, applicant's response must either comply

with all formal requirements or specifically traverse each requirement not complied with. See 37

C.F.R. § 1.111(b) and section 707.07(a) of the M.P.E.P.

Conclusion

6. The prior art made of record and not relied upon is considered pertinent to applicant's disclosure. Hallberg et al. (U.S. Patent 5,959,442) see particularly figures 1 and 2 and the corresponding disclosure and Ohsawa (U.S. Patent 4,906,914) show voltage regulators see particularly figure 5 and the corresponding disclosure and Lim (U.S. Patent 5,929,680) see particularly claim 1 especially i)4.

Any inquiry concerning this communication or earlier communications from the Examiner should be directed to Examiner Riley whose telephone number is 703.305.3487. The Examiner can normally be reached Monday through Thursday from 7:30-6:00 p.m. Eastern Standard Time. The fax phone number for this Group is 703.305.7731 or 7732. Any inquiry of a general nature or relating to the status of this application or proceeding should be directed to the Group receptionist whose telephone number is 703.308.1782.

Shawn Riley
Primary Examiner